

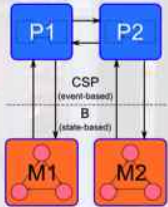
USING CSP||B AND ProB FOR RAILWAY MODELLING

Aims

"Natural modelling" to provide accessible and traceable formal specifications
 Tool support for verification with comprehensible feedback

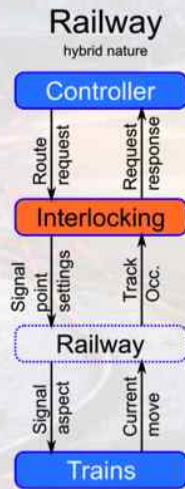
Background

CSP||B Architecture



Railway's duality

Event: "train moves on track T5"
 State condition: "signal S shows proceed if tracks T1, T2 are free"



A natural modelling approach

Driving rules

```

RW_CTRL = [] r : ROUTE @
(request!r -> RW_CTRL)

TRAIN_CTRL(t, currp) =
nextSignal!t?; ->
if (s == none or s == green)
then (move.t, currp?newp -> ...

ALL_TRAINS = [] t1 : TRACK @
enter.albert.t1 ->
((( t2 : diff(TRACK, exclude(t1)) @
enter.berrie.t2 ->
TRAIN_CTRL(albert, t1) |||
TRAIN_CTRL(berrie, t2)))

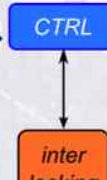
-TRL =
(enter?;t?p -> enter?t?p -> RW_CTRL)
[ {enter, request } | {
enter, nextSignal, move, stay } ]
ALL_TRAINS
    
```

Signalling

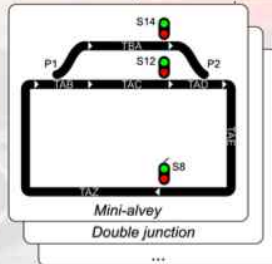
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INVARIANT
pos : TRAIN => TRACK &
...

OPERATIONS
enter(t,p) = ...
s <- nextSignal(t) = ...
currp, newp <- move(t) = ...
bb <- request(route) =
PRE route : ROUTE THEN
IF ((clearTable(route) << emptyTracks ))
THEN
LET unlockedPoints BE
unlockedPoints = POINTS-ran(currentLocks)
IN
...
    
```



Track plans



Datatypes

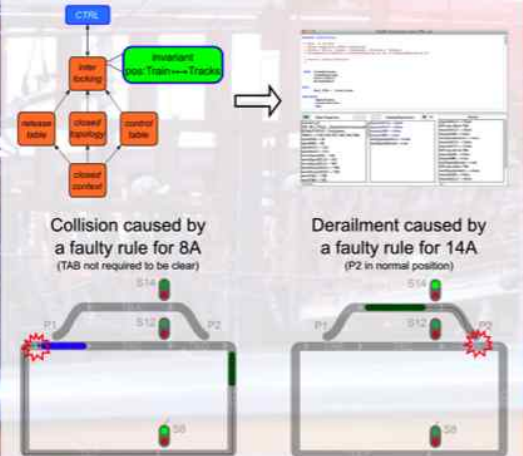
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SETS
TRACKSTATUS = {occ, empty};
ASPECT = {red, green, none};
ALLTRACK = {T8, TAB, TAC, ...};
SIGNAL = {S8, S12, S14};
TRAIN = {albert, berrie};
POINTS = {P201, P202};
POINTPOSITION = {normal, reverse};
POINTSTATUS = {locked, unlocked};
ROUTE = {A8, B8, A12, A14}

CONSTANTS
SIGNALSTATUS,
TRACK,
...
Mini-alveij
Double junction
...
    
```

Verification

Safety: no collision and no derailment



Results

CSP||B models which are
 - understandable and (thus) verifiable by our industrial partners
 - analysable by current verification technology

References:

- Schneider, S. and Treharne, H. *CSP theorems for communicating B machines*, Journal of Formal Aspects of Computing, Volume 17, Pages 390-422, 2005.
- F. Moller, H. Nguyen, M. Roggenbach, S. Schneider, H. Treharne, *Combining event-based and state-based modelling for railway verification*, Tech. Rep. CS-12-02, University of Surrey (2012).



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